

FIG. 1

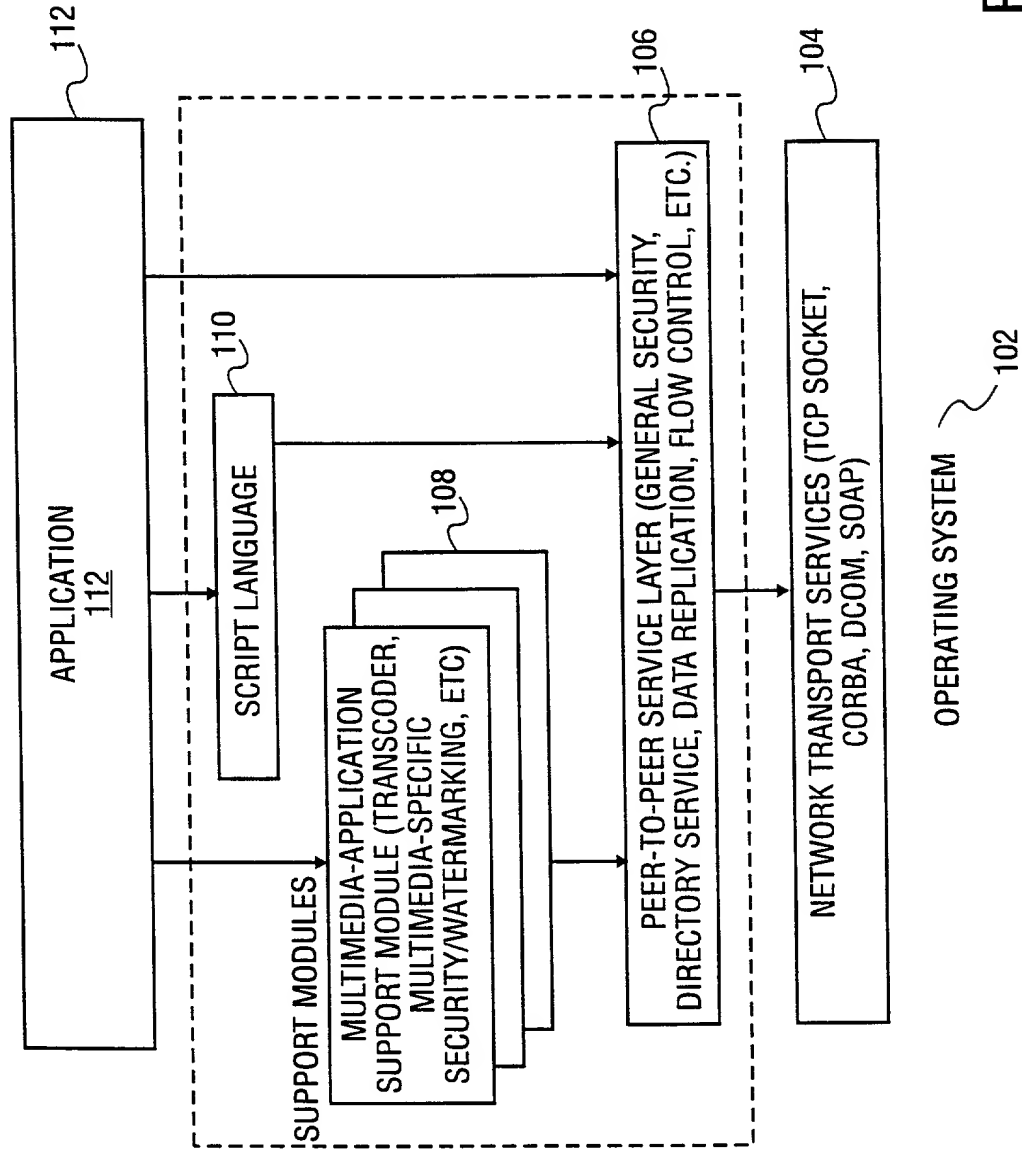
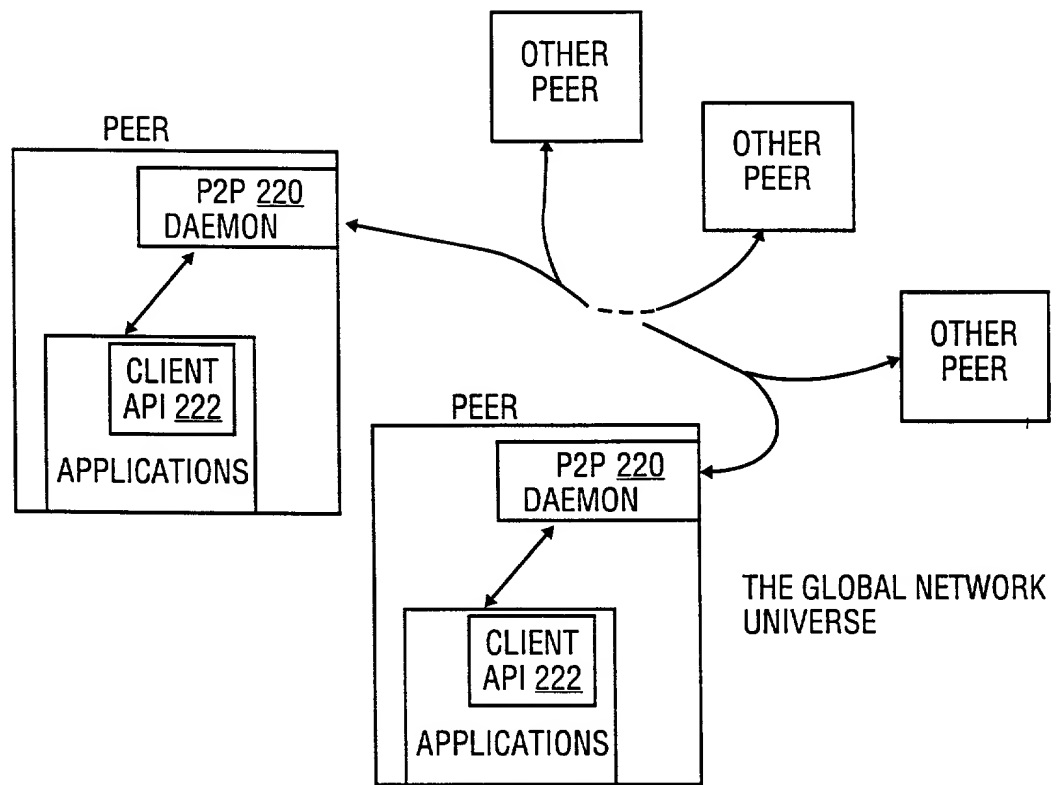


FIG. 2



OVERALL SYSTEM ARCHITECTURE

FIG. 3

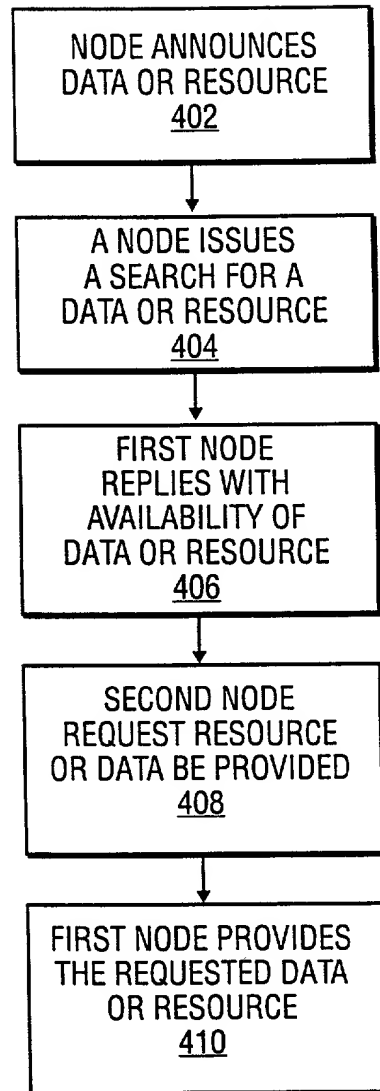


FIG. 4